

09/26/03

Case No. 02GR118254480

**MAIL STOP PATENT APPLICATION**  
**COMMISSIONER FOR PATENTS**  
**P.O. BOX 1450**  
**ALEXANDRIA, VA 22313-1450**

Submitted herewith for filing is the patent application of:

I HEREBY CERTIFY THIS PAPER OR FEE IS BEING DEPOSITED WITH THE U.S. POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 CFR 1.10 ON THE DATE INDICATED BELOW AND IS ADDRESSED TO: MAIL STOP PATENT APPLICATIONS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450.

EXPRESS MAIL NO: EV331552877US

DATE OF DEPOSIT: September 26, 2003

NAME: JUSTIN GOREE

SIGNATURE: Justin Goree

Inventors: **POIROUX ET AL.**

For: **PROCESS AND DEVICE FOR EVALUATING A CMOS LOGICAL CELL**

**Enclosed are:**

- ☒ Patent Application: 25 pages, 24 claims.  
☒ 8 sheets of drawings.  
☒ The suggested drawing figure to be published is FIG. 5.  
☒ A Preliminary Amendment.

Assignee info:

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Country of Incorporation: **France**

and

Name: **Commissariat A L'Energie Atomique**  
Address: **31/33 rue de la Fédération, 75752 Paris Cedex 15, France**  
Country of Incorporation: **France**

- ☒ Applicant claims priority benefit to the following foreign application(s):  
Country: **France**  
Application No.: **02 12022**  
Filing Date: **27 September 2002**

The Declaration and Filing Fee are **NOT ENCLOSED**.

- ☒ Name, Address and Citizenship of Inventor(s) is as follows:

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- ☒ **PLEASE ADDRESS ALL CORRESPONDENCE TO ATTORNEY OF RECORD: CHRISTOPHER F. REGAN**
- ☒ Associate this file with Customer No. 27975.

Date: September 26, 2003



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PATENT TRADEMARK OFFICE

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